

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Masayuki FURUHASHI et al.**

Serial Number: **Not Yet Assigned**

Filed: **October 29, 2003**

Customer No.: **38834**

For: **SEMICONDUCTOR DEVICE FABRICATION METHOD AND SEMICONDUCTOR
FABRICATION CONTROL METHOD**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

October 29, 2003

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,
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Enclosures: PTO-1449; References (6)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 032077	Serial No. New Appln.
	Applicant(s): Masayuki FURUHASHI et al.	
	Filing Date: October 29, 2003	Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)	
_____	AE	9-326396	12/16/97	Japan	Abstract.
_____	AF	2000-340644	12/08/00	Japan	Abstract
_____	AG	10-313012	11/24/98	Japan	Abstract
_____	AH	2001-102321	04/13/01	Japan	Abstract
_____	AI	2001-102386	04/13/01	Japan	Abstract
_____	AJ	2001-274154	10/05/01	Japan	Abstract
_____	AK				

OTHER DOCUMENTS

_____	AL	
_____	AM	

Examiner	Date Considered
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